



JFW

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Pritchard

Attorney Docket No.: ALTRP110/A1238

Application No.: 10/826,756

Examiner: Not assigned

Filed: April 15, 2004

Group: 2816

Title: METHODS AND APPARATUS FOR  
GENERATING PROGRAMMABLE DEVICE  
LAYOUT INFORMATION

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 8, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

Joyce L. Ferreira

**INFORMATION DISCLOSURE STATEMENT  
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ALTRP110).

Respectfully submitted,

BEYER WEAVER & THOMAS, LLP

Godfrey K. Kwan  
Registration No. 46,850

P.O. Box 70250  
Oakland, CA 94612-0250



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. ALTRP110/A1238 Applicant: Jeffrey Orion Pritchard Filing Date April 15, 2004	Application No.: 10/826,756  Group 2816
--	---	---

### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						
	A4						
	A5						

### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Braddock Gaskill, "Open Source PCB Design Tools", <a href="http://www.rdrop.com/~cary.mirror/tools_htmlized.html">www.rdrop.com/~cary.mirror/tools_htmlized.html</a> , September 24, 1999, 5 pages
	C2	
	C3	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.